



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/858,308	05/15/2001	Zahid Najam	10736/9	3090
757	7590 03/30/2005	EXAMINER		INER
BRINKS HOFER GILSON & LIONE			GERSTL, SHANE F	
P.O. BOX 10395 CHICAGO, IL 60610			ART UNIT	PAPER NUMBER
			2183	
			DATE MAIL ED: 03/30/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)	
09/858,308	NAJAM ET AL.	
Examiner	Art Unit	
Shane F Gerstl	2183	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --THE REPLY FILED 07 March 2005 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. 1. The reply was filed after a final rejection, but prior to filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods: months from the mailing date of the final rejection. The period for reply expires b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f). Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL 2. The reply was filed after the date of filing a Notice of Appeal, but prior to the date of filing an appeal brief. The Notice of Appeal A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of was filed on Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a). **AMENDMENTS** 3. The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because (a) They raise new issues that would require further consideration and/or search (see NOTE below); (b) They raise the issue of new matter (see NOTE below); (c) They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or (d) They present additional claims without canceling a corresponding number of finally rejected claims. NOTE: _____. (See 37 CFR 1.116 and 41.33(a)). 4. The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324). 5. Applicant's reply has overcome the following rejection(s): _____ 6. Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s). 7. Tor purposes of appeal, the proposed amendment(s): a) will not be entered, or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended. The status of the claim(s) is (or will be) as follows: Claim(s) allowed: Claim(s) objected to: Claim(s) rejected: _ Claim(s) withdrawn from consideration: _____. AFFIDAVIT OR OTHER EVIDENCE 8. The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e). 9. The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing a good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1). 10. The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached. REQUEST FOR RECONSIDERATION/OTHER 11. The request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet. 12. Note the attached Information Disclosure Statement(s). (PTO/SB/08 or PTO-1449) Paper No(s). 13. ☐ Other: .

Continuation of 11. does NOT place the application in condition for allowance because: Applicants argument regarding claims 1, 11, 24, and 34 is not persuasive. Applicant has argued that since the processor accesses memory using a random access port which must be interrupted for the coprocessor to access the memory via the serial port, that the memory accesses do not occur "substantially simultaneously". Applicant has provided the definition of "substantially" to be "being largely but not wholly that which is specified" and the definition of "simultaneous" to be "existing or occuring at the same time". Therefore the definition of "substantially simultaneously" would be "largely but not wholly existing or occuring at the same time. Applicant has then argued that since a time period in a processor is defined by a clock cycle, "substantially simultaneously" refers to access that occurs within the same clock cycle buit at different moments in time. The Examiner asserts that another interpretation of simultaneously also applies with the above definition. The claim language requires that the memory accesses of the processor and coprocessor (via the random access port and serial port of Rutman) be largely but not wholly existing or occuring at the same time. However, simply because processors typically use a standardized clock cycle as a unit or period of time, this is not always the case, nor does the definition even provide for the occurance to happen in the same standardized time period but only refers to a generic "time" which is a relative term. One of ordinary skill in the art would recognize that a processor typically executes trillions of clock cycles. Therefore, the two memory access occuring within two cycles among trillions of cycles is so miniscule that these accesses can rightfully be called substantially simultaneous accesses.

EDDIE CHAN

SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100